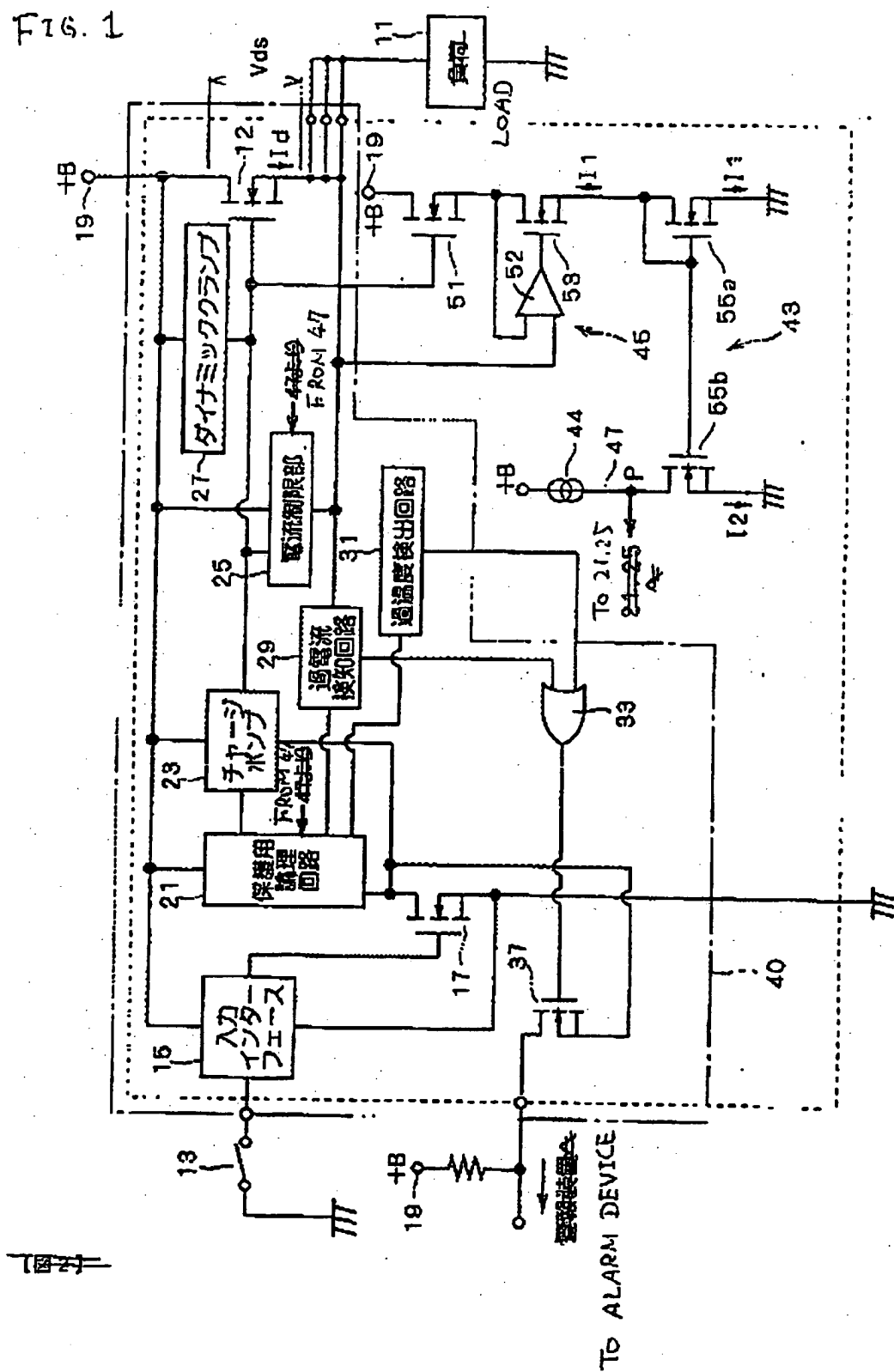


FIG. 1



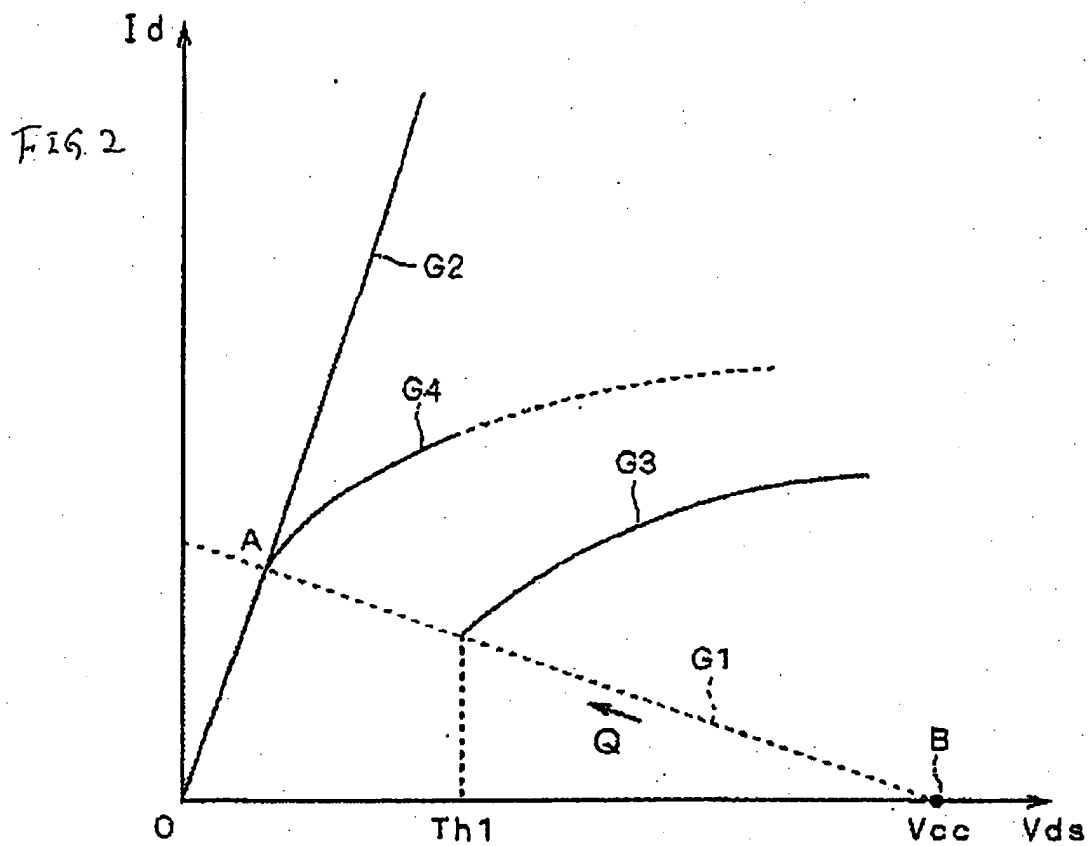


FIG. 3

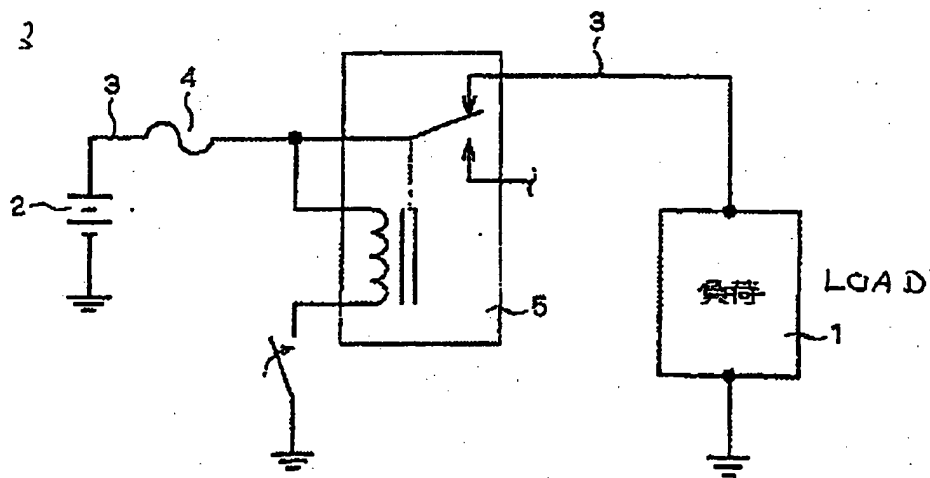
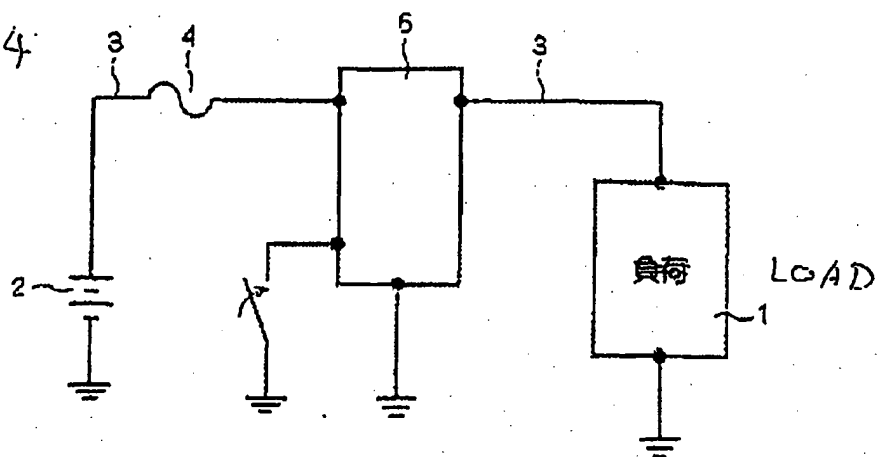
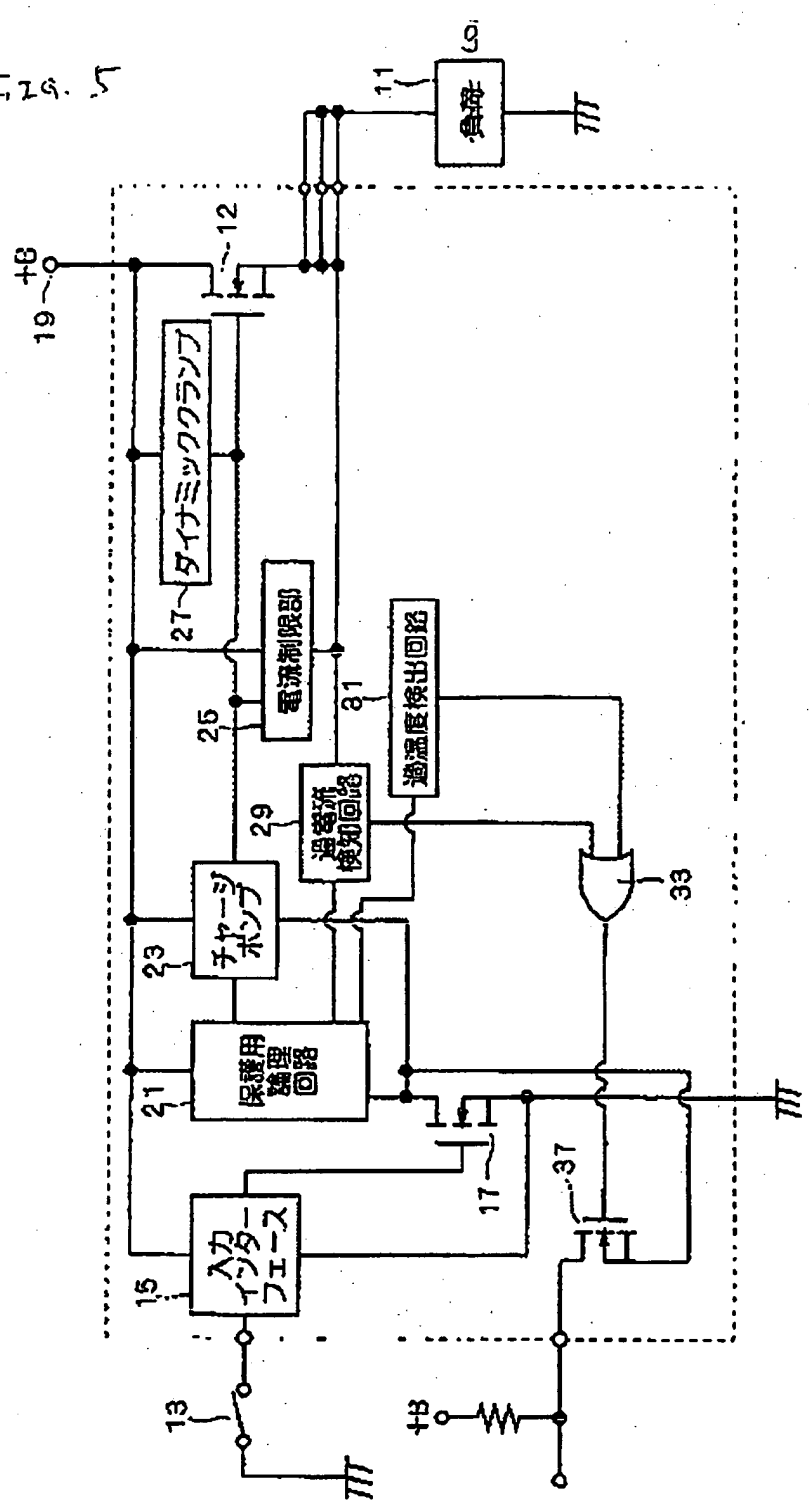


FIG. 4



〔図5〕

Fig. 5



【発明名称】 要約書

【要約】

【課題】

比較的低い電圧の領域においても電流制限を実行する。

FIG. 1

- 15 INPUT INTERFACE
- 21 PROTECTIVE LOGIC CIRCUIT
- 5 23 CHARGE PUMP
- 25 CURRENT LIMITER
- 27 DYNAMIC CLAMP CIRCUIT
- 29 OVERCURRENT DETECTING CIRCUIT
- 31 OVERTEMPERATURE DETECTING CIRCUIT

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FIG. 5

- 15 INPUT INTERFACE
- 21 PROTECTIVE LOGIC CIRCUIT
- 5 23 CHARGE PUMP
- 25 CURRENT LIMITER
- 27 DYNAMIC CLAMP CIRCUIT
- 29 OVERCURRENT DETECTING CIRCUIT
- 31 OVERTEMPERATURE DETECTING CIRCUIT

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